

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	89	((Built adj in adj self adj test) BIST) and (ECC (error adj correction adj code))	USPAT	OR	OFF	2006/11/21 09:20
S2	394	(processor adj3 ((self adj test) BIST))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 14:23
S3	27	(processor adj8 ((self adj test) BIST)) and (((error adj correction adj code) ECC) with memor\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 14:10
S4	889	714/30.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 14:52
S5	312	714/31.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 14:52
S6	22	(S4 S5) and S2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 14:15
S7	76	(processor adj3 ((self adj test) BIST)) and ((multi dual) adj (core processor))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 14:24

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S8	294	(S4 S5) and ((self adj test) BIST)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 14:54
S9	411	((Built adj in adj self adj test) BIST) and (CRC ECC (error adj correction adj code)) and processor and (memory cache)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 15:06
S10	7	((Built adj in adj self adj test) BIST) with processor and ((CRC ECC (error adj correction adj code)) with (memory cache))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 15:09
S11	1	"10/763903" and (self adj test)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 15:10
S12	39	((Built adj in adj self adj test) BIST) same (CRC ECC (error adj correction adj code))	USPAT	OR	OFF	2006/11/16 15:13
S13	94	((Built adj in adj self adj test) BIST) and (ECC ECU (error adj correction adj (unit code)))	USPAT	OR	OFF	2006/11/16 16:05
S14	6	((Built adj in adj self adj test) BIST) and (ECU (error adj correction adj unit))	USPAT	OR	OFF	2006/11/16 16:05
S15	444	((self adj test) BIST) and (ECC ECU (error adj correction adj (unit code)))	USPAT	OR	OFF	2006/11/16 16:16
S16	10	processor adj BIST	USPAT	OR	OFF	2006/11/16 16:08
S17	28	processor adj BIST	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 16:08

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S18	0	(plurality with processor with ((self adj test) BIST))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 16:17
S20	91	(plurality adj3 ((self adj test) BIST)) and processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 16:18
S21	189	(plurality adj3 ((self adj test) BIST))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 16:18
S22	9	(plurality adj3 ((self adj test) BIST)) same processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 16:59
S23	72	(Harter adj3 Werner).in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/16 17:00
S24	139	(BIST (self adj test\$3)) with (plurality adj2 (elements components processor memory RAM ROM))	USPAT	OR	ON	2006/11/17 11:31
S25	66	((BIST (self adj test\$3)) with plurality with processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/17 11:43

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S26	41	((BIST (self adj test\$3)) same ((error adj correction) ECC) same processor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/17 11:54
S27	121	plurality adj3 self adj test	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/17 11:54
S28	12	(ECC ABIST) same ((transmit\$3 send\$3) with (error adj (message report signal)) with process\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 09:24
S29	24	(SOC IC) same ((transmit\$3 send\$3) with (error adj (message report signal)) with process\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 09:29
S30	1	BIST same ((transmit\$3 send\$3) with (error adj (message report signal)) with process\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 09:26
S31	5433	(external adj display)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 09:26
S32	9	(memory with error) same (external adj display)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 09:26

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S33	505	(SOC (system adj3 chip) IC (integrated adj circuit)) and ((transmit\$3 send\$3) with (error adj (message report signal)) with process\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 09:59
S34	32	(SOC (system adj3 chip) IC (integrated adj circuit)) and ((transmit\$3 send\$3) with (error adj (message report signal)) with memory with process\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 09:31
S37	661	(plurality with ((self adj test) BIST)) and (SOC (system adj3 chip) IC (integrated adj circuit))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 10:02
S39	368	"714"/\$.ccIs. and S37	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 10:03
S42	72	(Harter adj3 Werner).in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 10:03
S43	14	S42 and safety	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/21 10:04
S44	1698	ECC same ((transmit\$3 send\$3 report\$3) with (error fault))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 09:54

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S45	6	(self adj test\$3) same ECC same ((transmit\$3 send\$3 report\$3 display\$3) with (error fault))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:38
S46	1	(self adj test\$3) same ECC same ((transmit\$3 send\$3 report\$3 display\$3) with (error fault) with process\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:39
S47	4433	(ECC EDU (error adj3 (detection correction))) same ((transmit\$3 send\$3 report\$3 display\$3) with (error fault) with process\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:40
S48	69	(ECC EDU (error adj3 (detection correction))) same ((transmit\$3 send\$3 report\$3 display\$3) with (error fault) with process\$3) and (memory adj management)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:41
S50	32	((transmit\$3 send\$3 report\$3 display\$3) with (error fault)) same (memory adj management) same processor	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:42
S51	53	((transmit\$3 send\$3 report\$3 display\$3) with (error fault)) same (memory adj management) same (process\$3 CPU)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:45
S52	6	((transmit\$3 send\$3 report\$3 display\$3) with (error fault)) same (memory adj management) same (process\$3 CPU) same ECC	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:46

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S53	13	((display\$3) with (error fault)) same (memory adj management) same (process\$3 CPU)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:49
S54	8139	(memory adj management) with (process\$3 CPU)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:49
S55	2	(memory adj management) with (microprocessor process\$3 CPU) with ((display\$3 report\$3) adj3 (error fault))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:51
S56	7	(memory adj management) with ((display\$3 report\$3) adj3 (error fault))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:51